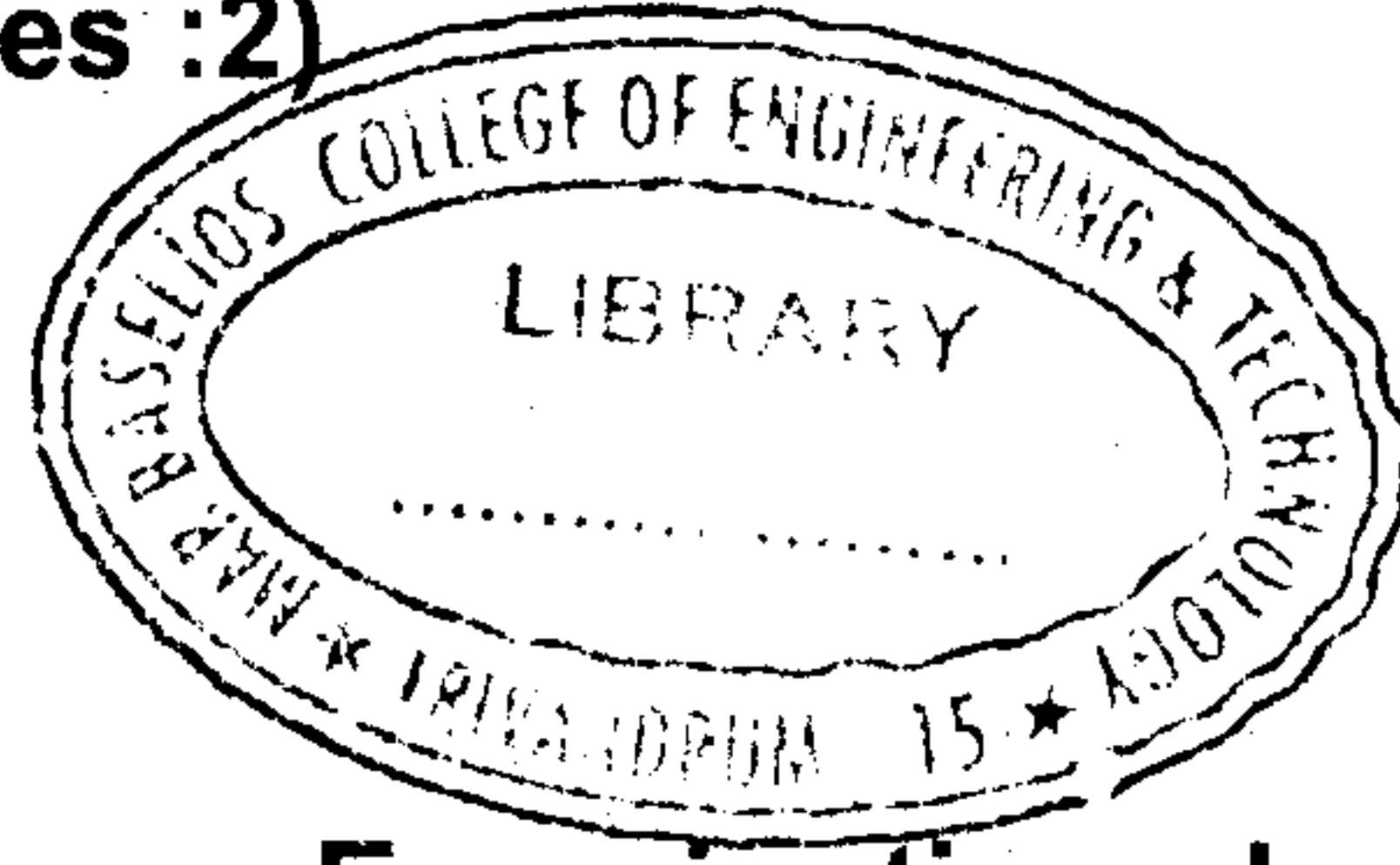


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Reg. No. :

Name :



Fourth Semester B.Tech Degree Examination, June 2019

(2013 Scheme)

13.402 COMPUTER ORGANIZATION AND DESIGN (FR)

Time : 3 Hours

Max. Marks :100

PART - A

I. Answer **All** questions : Each question carries 4 marks.

1. Explain the stored program concept.
2. Construct a 1 bit ALU.
3. Write the steps needed for executing R type instruction.
4. State 2 techniques to reduce the cache miss penalty.
5. What are the differences between subroutine and interrupt service routine?

(5 × 4 = 20 Marks)

PART - B

II. Answer **ONE** questions from each module

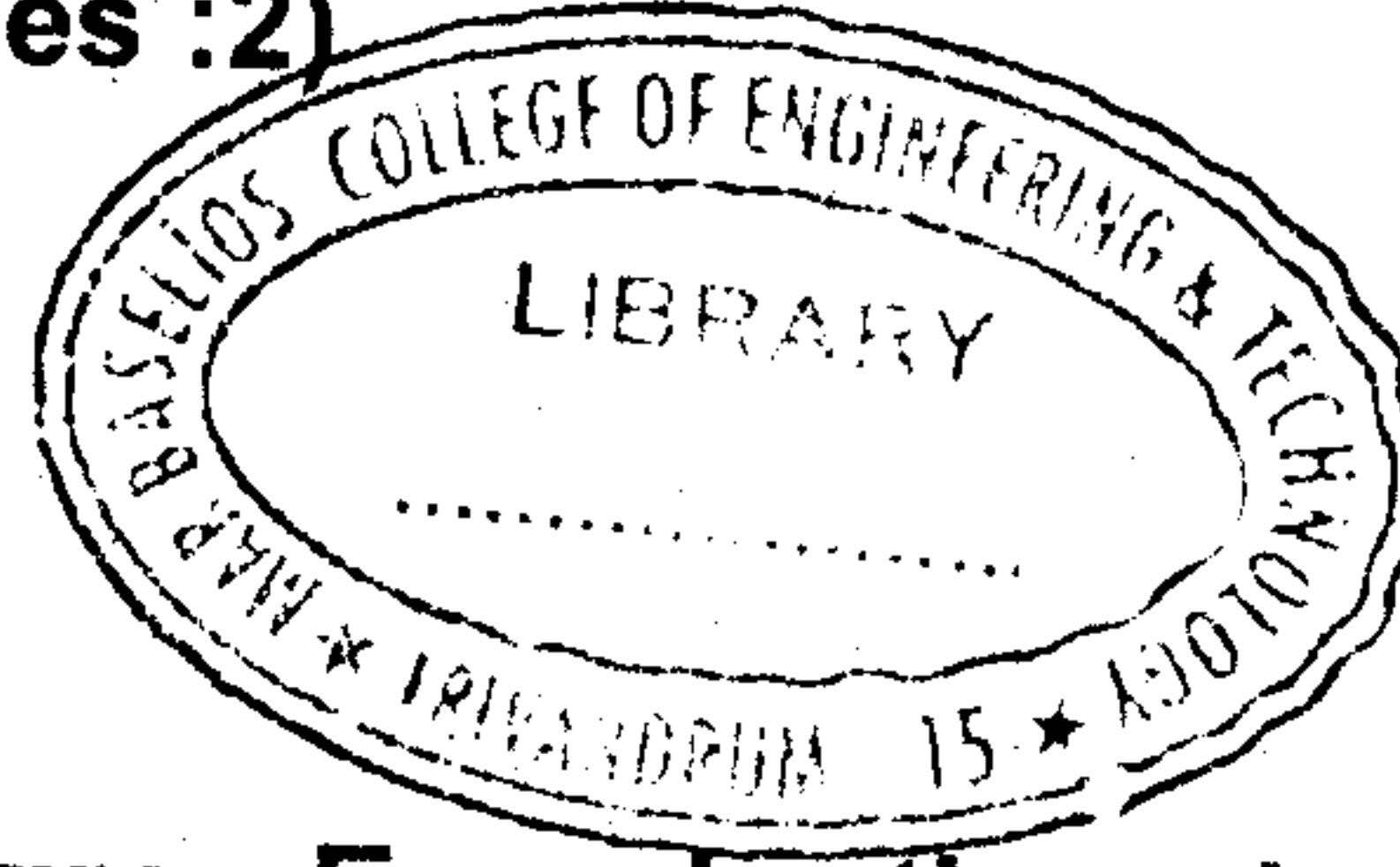
Module I

6. (a) Draw the block diagram and explain the steps involved in basic operational concepts. **(10)**
- (b) What are the functional units of a computer? Explain briefly. **(10)**

P.T.O.

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Module I

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