PART – A

Answer all questions.

1. Explain the different modes of 8255 interface chip.
2. Explain vectored interrupt.
3. Explain the different functions of 8259 interrupt controller.
4. What are the advantages of D.M.A. execution?
5. Explain the transmission format for serial communication.
6. Explain character generation for CRT operation.
7. What are the different modes of 8279 keyboard display interface?
8. Explain pipelining in 8086 CPU.
9. Explain how 20 bit physical address is generated in 8086 CPU.
10. Explain the different arithmetic instructions in 8086 processes. (10×4=40 Marks)

PART – B

Answer one full question from each Module.

Module – I

11. Draw the internal architecture of 8155 programmable peripheral interface and explain its different functions. 20
12. What are the different modes of operation of 8253 timer? Explain with output wave forms. 20

P.T.O.
Module – II

13. Draw the architecture of 8257 DMA controller and explain how it can be interfaced with a processor. 20

14. Explain the working of CRT controller INTEL 8275. 20

Module – III

15. With neat circuit diagram explain how 8279 can be used to interface 16 nos. of keys and six numbers of 7 segment display with a processor. 20

16. What are the different addressing modes in 8086? Explain with examples. 20